

PATENT NUMBER and ISSUE DATE

## U.S. UTILITY Patent Application

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	*APPLICANTS: Karasawa Junichi; Watanabe Kunio:									
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	JAPAN 2001-330784(P) 10/29/2001									
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	35 LISC 1-9 conditions met									
	Verified and Adinowledged Examiners siint als 81751 0026									
	TITLE: Semiconductor device, memory system and electronic apparatus									

NOTICE OF ALL	OWANCE MAILED	THINH I NOUYEN	CLAIMS ALLOWED					
		Assistant Examiner	Total Claims	Prin O.G	t Claim for			
ISS	UE FEE		DRAWING					
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.			
	<u> </u>	Primary Examiner	Application Examiner					
▼ TEF	RMINAL	PREPARED FOR ISSUE						
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